



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 3633**  
Satoshi SHIDA et al. : Docket No. 2002\_0278A  
Serial No. 10/069,401 : Group Art Unit 2812  
Filed February 26, 2002 : Examiner Angel Roman

METHOD AND APPARATUS FOR  
HANDLING ARRAYED COMPONENTS

THE COMMISSIONER IS AUTHORIZED  
TO CHARGE ANY DEFICIENCY IN THE  
FEES FOR THIS PAPER TO DEPOSIT  
ACCOUNT NO. 23-0975

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement of September 23, 2003, Applicants hereby elect Group I, which is drawn to a method of making a semiconductor device and is embodied by claims 1-4, 17, 18, 31 and 32.

Having made the required election, a full examination on the merits of the elected group is requested.

Respectfully submitted,

Satoshi SHIDA et al.

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